19-1221; Rev 3; 8/01

Single/Dual/Quad, 270MHz, 1mA, SOT23, Current-Feedback Amplifiers with Shutdown

General Description

The MAX4180 family of current-feedback amplifiers combines high-speed performance, low distortion, and excellent video specifications with ultra-low-power operation in miniature packages. They operate from ±2.25V to ±5.5V dual supplies, or from a single +5V supply. They require only 1mA of supply current per amplifier while delivering up to ±60mA of output current drive. The MAX4180/MAX4182/MAX4183/MAX4186 are compensated for applications with a closed-loop gain of +2 (6dB) or greater, and provide a -3dB bandwidth of 240MHz and a 0.1dB bandwidth of 70MHz. The MAX4181/MAX4184/MAX4185/MAX4187 are compensated for applications with a +1 (0dB) or greater gain, and provide a -3dB bandwidth of 270MHz and a 0.1dB bandwidth of 60MHz.

The MAX4180–MAX4187 feature 0.08%/0.03° differential gain and phase errors, a 20ns settling time to 0.1%, and a 450V/µs slew rate, making them ideal for high-performance video applications. The MAX4180/MAX4181/MAX4183/MAX4185 have a low-power shutdown mode that reduces power-supply current to 135µA and places the outputs in a high-impedance state. This feature makes them ideal for multiplexing applications.

The single MAX4180/MAX4181 are offered in space-saving 6-pin SOT23 packages.

Applications

- Portable/Battery-Powered Video/Multimedia Systems
- Broadcast and High-Definition TV Systems
- High-Speed A/D Buffers
- CCD Imaging Systems
- Medical Imaging
- High-Definition Surveillance Video
- Professional Cameras
- Video Switching/Multiplexing

Selector Guide

PART	NO. OF AMPS	SHUTDOWN MODE	OPTIMIZED FOR
MAX4180	1	Yes	$A_V \geq 2$
MAX4181	1	Yes	$A_V \geq 1$
MAX4182	2	No	$A_V \geq 2$
MAX4183	2	Yes	$A_V \geq 2$
MAX4184	2	No	$A_V \geq 1$
MAX4185	2	Yes	$A_V \geq 1$
MAX4186	4	No	$A_V \geq 2$
MAX4187	4	No	$A_V \geq 1$

Features

- ♦ **Ultra-Low Supply Current: 1mA per Amplifier**
- ♦ **Shutdown Mode**
  - **Outputs Placed in High-Z**
  - **Supply Current Reduced to 135µA**
- ♦ **Operate from a Single +5V Supply or Dual ±5V Supplies**
- ♦ **Wide Bandwidth**
  - **270MHz -3dB Small-Signal Bandwidth (MAX4181/MAX4184/MAX4185/MAX4187)**
- ♦ **450V/µs Slew Rate**
- ♦ **Fast, 20ns Settling Time to 0.1%**
- ♦ **Excellent Video Specifications**
  - **Gain Flatness to 70MHz (MAX4180/MAX4182/MAX4183/MAX4186)**
  - **0.08%/0.03° Differential Gain/Phase**
- ♦ **Low Distortion:**
  - **-73dBc SFDR (f_C = 5MHz, V_{OUT} = 2Vp-p)**
- ♦ **Available in Tiny Surface-Mount Packages**
  - **6-Pin SOT23 (MAX4180/MAX4181)**
  - **10-Pin µMAX (MAX4183/MAX4185)**
  - **16-Pin QSOP (MAX4186/MAX4187)**

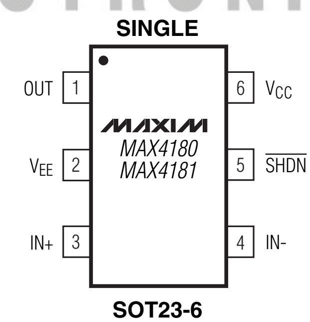
Ordering Information

PART	TEMP RANGE	PIN-PACKAGE	TOP MARK
MAX4180EUT-T	-40°C to +85°C	6 SOT23-6	AAAB
MAX4180ESA	-40°C to +85°C	8 SO	—

Ordering Information continued at end of data sheet.

Pin Configurations

TOP VIEW



Pin Configurations continued at end of data sheet.

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

ABSOLUTE MAXIMUM RATINGS

Supply Voltage (V_{CC} to V_{EE}) 12V
Analog Input Voltage ....(V_{EE} - 0.3V) to (V_{CC} + 0.3V)
Differential Input Voltage ....±2V
$\overline{SHDN}$ Input Voltage ....(V_{EE} - 0.3V) to (V_{CC} + 0.3V)
Short-Circuit Duration (OUT to GND, V_{CC} or V_{EE}).....Continuous
Continuous Power Dissipation (T_A = +70°C)
6-Pin SOT23 (derate 7.10mW/°C above +70°C)....571mW
8-Pin SO (derate 5.88mW/°C above +70°C)....471mW
10-Pin µMAX (derate 5.60mW/°C above +70°C)....444mW
14-Pin SO (derate 8.33mW/°C above +70°C)....667mW
16-Pin QSOP (derate 8.30mW/°C above +70°C)....667mW
Operating Temperature Range ....-40°C to +85°C
Storage Temperature Range ....-65°C to +150°C
Lead Temperature (soldering, 10s) ....+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

DC ELECTRICAL CHARACTERISTICS—Dual Supplies

(V_{CC} = +5V, V_{EE} = -5V, V_{IN+} = 0, $\overline{SHDN}$ ≥ 3V; T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Voltage Range	V_{CM}	Guaranteed by CMRR test	±3.6	±3.9		V
Input Offset Voltage	V_{OS}	V_{CM} = 0		±1.5	±7	mV
Input Offset-Voltage Drift	TC_{VOS}			±12		µV/°C
Input Offset-Voltage Matching		MAX4182–MAX4187		±1		mV
Input Bias Current (Positive Input)	I_{B+}			±1	±7	µA
Input Bias Current (Negative Input)	I_{B-}			±1	±12	µA
Input Resistance (Positive Input)	R_{IN+}	-3.6V ≤ V_{IN+} ≤ 3.6V, -1V ≤ (V_{IN+} - V_{IN-}) ≤ 1V	250	800		kΩ
Input Resistance (Negative Input)	R_{IN-}			160		Ω
Common-Mode Rejection Ratio	CMRR	-3.6V ≤ V_{CM} ≤ 3.6V	-50	-58		dB
Open-Loop Transresistance	T_R	R_L = 1kΩ, V_{OUT} = ±3.6V	0.8	3.0		MΩ
		R_L = 150Ω, V_{OUT} = ±2.5V	0.3	0.9		
Output Voltage Swing	V_{SW}	R_L = 1kΩ	±3.75	±4.0		V
		R_L = 150Ω	±3.0	±3.3		
		R_L = 100Ω		±3.0		
Output Current	I_{OUT}	R_L = 30Ω	±32	±60		mA
Output Short-Circuit Current	I_{SC}			±80		mA
Output Resistance	R_{OUT}			0.2		Ω
Disabled Output Leakage Current	$I_{OUT(OFF)}$	$\overline{SHDN}$ ≤ V_{IL}, V_{OUT} ≤ ±3V (Notes 2, 4)		±0.1	±6.0	µA
$\overline{SHDN}$ Logic Low Threshold	V_{IL}	(Notes 3, 4)			V_{CC} - 3.0	V
$\overline{SHDN}$ Logic High Threshold	V_{IH}	(Notes 3, 4)	V_{CC} - 2.0			V

DC ELECTRICAL CHARACTERISTICS—Dual Supplies (continued)

(V_{CC} = +5V, V_{EE} = -5V, V_{IN+} = 0, $\overline{SHDN}$ ≥ 3V; T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
$\overline{SHDN}$ Logic Input Bias Current	I_{IN}	V_{EE} ≤ $\overline{SHDN}$ ≤ V_{CC} (Note 4)			±0.1	±2.0	µA
Positive Power-Supply Rejection Ratio	PSRR+	V_{EE} = -5V, V_{CC} = 4.5V to 5.5V		60	71		dB
Negative Power-Supply Rejection Ratio	PSRR-	V_{CC} = 5V, V_{EE} = -4.5V to -5.5V		53	62		dB
Operating Supply Voltage	V_{CC}/V_{EE}			±2.25		±5.50	V
Quiescent Supply Current per Amplifier	I_S	R_L = ∞	MAX418_EUT		1.0	1.3	mA
			All other packages		1.0	1.2	
Shutdown Supply Current per Amplifier	$I_{S(OFF)}$	$\overline{SHDN}$ = 0, R_L = ∞ (Note 4)			135	180	µA

DC ELECTRICAL CHARACTERISTICS—Single Supply

(V_{CC} = +5V, V_{EE} = 0, V_{IN+} = 2.5V, $\overline{SHDN}$ ≥ 3V, R_L to $V_{CC}/2$; T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Voltage Range	V_{CM}		1.3 to 3.7	1.1 to 3.9		V
Input Offset Voltage	V_{OS}	V_{CM} = 2.5V		±1.5	±7	mV
Input Offset Voltage Drift	TC_{VOS}			±12		µV/°C
Input Offset Voltage Matching		MAX4182–MAX4187		±1		mV
Input Bias Current (Positive Input)	I_{B+}			±1	±7	µA
Input Bias Current (Negative Input)	I_{B-}			±1	±12	µA
Input Resistance (Positive Input)	R_{IN+}	1.3V ≤ V_{IN+} ≤ 3.7V, -1V ≤ (V_{IN+} - V_{IN-}) ≤ 1V	250	800		kΩ
Input Resistance (Negative Input)	R_{IN-}			160		Ω
Common-Mode Rejection Ratio	CMRR	1.3V ≤ V_{CM} ≤ 3.7V	-50	-58		dB
Open-Loop Transresistance	T_R	R_L = 1kΩ, V_{OUT} = 1.2V to 3.8V	0.8	2.5		MΩ
		R_L = 150Ω, V_{OUT} = 1.4V to 3.6V	0.275	0.9		
Output Voltage Swing	V_{SW}	R_L = 1kΩ	1.15 to 3.85	1.0 to 4.0		V
		R_L = 150Ω	1.35 to 3.65	1.2 to 3.8		
		R_L = 100Ω		1.3 to 3.7		

DC ELECTRICAL CHARACTERISTICS—Single Supply (continued)

(V_{CC} = +5V, V_{EE} = 0, V_{IN+} = 2.5V, $\overline{SHDN}$ ≥ 3V, R_L to $V_{CC}/2$; T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Output Current	I_{OUT}	R_L = 30Ω		±18	±30		mA
Output Short-Circuit Current	I_{SC}				±50		mA
Output Resistance	R_{OUT}				0.2		Ω
Disabled Output Leakage Current	$I_{OUT(OFF)}$	$\overline{SHDN}$ ≤ V_{IL}, 1.2V ≤ V_{OUT} ≤ 3.8V (Notes 2, 4)			±0.1	±4.0	µA
$\overline{SHDN}$ Logic-Low Threshold	V_{IL}	(Notes 3, 4)				V_{CC} - 3.0	V
$\overline{SHDN}$ Logic-High Threshold	V_{IH}	(Notes 3, 4)		V_{CC} - 2.0			V
$\overline{SHDN}$ Logic Input Bias Current	I_{IN}	0 ≤ $\overline{SHDN}$ ≤ V_{CC} (Note 4)			±0.1	±2.0	µA
Power-Supply Rejection Ratio	PSRR	V_{CC} = 4.5V to 5.5V		60	71		dB
Operating Supply Voltage	V_{CC}			4.5		5.5	V
Quiescent Supply Current per Amplifier	I_S	R_L = ∞	MAX418_EUT		1.0	1.25	mA
			All other packages		1.0	1.2	
Shutdown Supply Current per Amplifier	$I_{S(OFF)}$	$\overline{SHDN}$ = 0, R_L = ∞ (Note 4)			135	180	µA

AC ELECTRICAL CHARACTERISTICS—Dual Supplies (MAX4180/4182/4183/4186)

(V_{CC} = +5V, V_{EE} = -5V, V_{IN} = 0, $\overline{SHDN}$ ≥ 3V, A_V = +2V/V; see Table 1 for R_F and R_G values; T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Small-Signal -3dB Bandwidth (Note 5)	BW_{SS}	<0.5dB peaking	R_L=1kΩ	180	245		MHz
			R_L=150Ω		190		
Large-Signal -3dB Bandwidth	BW_{LS}	V_{OUT} = 2Vp-p, R_L = 1kΩ			150		MHz
Bandwidth for 0.1dB Flatness (Note 5)	$BW_{0.1dB}$	R_L = 1kΩ		30	70		MHz
		R_L = 150Ω			70		
Slew Rate (Note 5)	SR	V_{OUT} = 2V step, R_L = 1kΩ	Rising edge	340	450		V/µs
			Falling edge	315	420		
Settling Time to 0.1%	t_S	V_{OUT} = 2V step, R_L = 1kΩ			20		ns
Rise/Fall Time	t_R, t_F	V_{OUT} = 2V step, R_L = 1kΩ			5		ns
Spurious-Free Dynamic Range	SFDR	f_C = 5MHz, V_{OUT} = 2Vp-p	R_L = 1kΩ		73		dBc
			R_L = 150Ω		57		
Second Harmonic Distortion		f_C = 5MHz, V_{OUT} = 2Vp-p	R_L = 1kΩ		-83		dBc
			R_L = 150Ω		-68		
Third Harmonic Distortion		f_C = 5MHz, V_{OUT} = 2Vp-p	R_L = 1kΩ		-73		dBc
			R_L = 150Ω		-57		

AC ELECTRICAL CHARACTERISTICS—Dual Supplies (MAX4180/4182/4183/4186) (cont.)

(V_{CC} = +5V, V_{EE} = -5V, V_{IN} = 0, $\overline{SHDN}$ ≥ 3V, A_V = +2V/V; see Table 1 for R_F and R_G values; T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Differential Phase Error	DP	NTSC	R_L = 1kΩ		0.03		degrees
			R_L = 150Ω		0.30		
Differential Gain Error	DG	NTSC	R_L = 1kΩ		0.08		%
			R_L = 150Ω		0.01		
Input Noise-Voltage Density	e_n	f = 10kHz			2		nV/√Hz
Input Noise-Current Density	i_n	f = 10kHz	IN+		4		pA/√Hz
			IN-		5		
Input Capacitance (Positive Input)	C_{IN+}				1.5		pF
Output Impedance	Z_{OUT}	f = 10kHz			4.8		Ω
Disabled Output Capacitance	$C_{OUT(OFF)}$	$\overline{SHDN}$ ≤ V_{IL}, V_{OUT} ≤ ±3V (Notes 2, 4)			4		pF
Turn-On Time from $\overline{SHDN}$	t_{ON}	(Note 4)			40		ns
Turn-Off Time to $\overline{SHDN}$	t_{OFF}	(Note 4)			400		ns
Power-Up Time					200		µs
Off-Isolation		$\overline{SHDN}$ ≤ 2V, R_L = 150Ω, f = 10MHz			-60		dB
Crosstalk		f = 10MHz, MAX4182/4183/4186			-60		dB
Gain Matching to 0.1dB		f = 10MHz, MAX4182/4183/4186			25		MHz

AC ELECTRICAL CHARACTERISTICS—Dual Supplies (MAX4181/4184/4185/4187)

(V_{CC} = +5V, V_{EE} = -5V, V_{IN+} = 0, $\overline{SHDN}$ ≥ 3V, A_V = +1V/V; see Table 1 for R_F values; T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Small-Signal -3dB Bandwidth (Note 5)	BW_{SS}	<0.5dB peaking	R_L = 1kΩ	195	270		MHz
			R_L = 150Ω		205		
Large-Signal -3dB Bandwidth	BW_{LS}	V_{OUT} = 2Vp-p, R_L = 1kΩ			90		MHz
Bandwidth for 0.1dB Flatness (Note 5)	$BW_{0.1dB}$	R_L = 1kΩ		20	60		MHz
		R_L = 150Ω			55		
Slew Rate (Note 5)	SR	V_{OUT} = 2V step, R_L = 1kΩ	Rising edge	250	320		V/µs
			Falling edge	200	265		
Settling Time to 0.1%	t_S	V_{OUT} = 2V step, R_L = 1kΩ			21		ns
Rise/Fall Time	t_R and t_F	V_{OUT} = 2V step, R_L = 1kΩ			5		ns
Spurious-Free Dynamic Range	SFDR	f_C = 5MHz, V_{OUT} = 2Vp-p	R_L = 1kΩ		57		dB
			R_L = 150Ω		66		
Second Harmonic Distortion		f_C = 5MHz, V_{OUT} = 2Vp-p	R_L = 1kΩ		-70		dB
			R_L = 150Ω		-73		
Third Harmonic Distortion		f_C = 5MHz, V_{OUT} = 2Vp-p	R_L = 1kΩ		-57		dB
			R_L = 150Ω		-66		
Differential Phase Error	DP	NTSC	R_L = 1kΩ		0.01		degrees
			R_L = 150Ω		0.48		

AC ELECTRICAL CHARACTERISTICS—Dual Supplies (MAX4181/4184/4185/4187) (cont.)

(V_{CC} = +5V, V_{EE} = -5V, V_{IN+} = 0, $\overline{SHDN}$ ≥ 3V, A_V = +1V/V; see Table 1 for R_F values; T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Differential Gain Error	DG	NTSC	R_L = 1kΩ		0.09		%
			R_L = 150Ω		0.16		
Input Noise-Voltage Density	e_n	f = 10kHz			2		nV/√Hz
Input Noise-Current Density	i_n	f = 10kHz	IN+		4		pA/√Hz
			IN-		5		
Input Capacitance (Positive Input)	C_{IN+}				1.5		pF
Output Impedance	Z_{OUT}	f = 10kHz			4.8		Ω
Disabled Output Capacitance	$C_{OUT(OFF)}$	$\overline{SHDN}$ ≤ V_{IL}, V_{OUT} ≤ ±3V (Notes 2, 4)			4		pF
Turn-On Time from $\overline{SHDN}$	t_{ON}	(Note 4)			50		ns
Turn-Off Time to $\overline{SHDN}$	t_{OFF}	(Note 4)			400		ns
Power-Up Time					200		µs
Off-Isolation		$\overline{SHDN}$ ≤ 2V, R_L = 150Ω, f = 10MHz			-54		dB
Crosstalk		f = 10MHz, MAX4184/MAX4185/MAX4187			-60		dB
Gain Matching to 0.1dB		f = 10MHz, MAX4184/MAX4185/MAX4187			25		MHz

AC ELECTRICAL CHARACTERISTICS—Single Supply (MAX4180/4182/4183/4186)

(V_{CC} = +5V, V_{EE} = 0, V_{IN+} = 2.5V, $\overline{SHDN}$ ≥ 3V, A_V = +2V/V; see Table 1 for R_F and R_G values; T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Small-Signal -3dB Bandwidth (Note 5)	BW_{SS}	<0.5dB peaking	R_L = 1kΩ	155	210		MHz
			R_L = 150Ω		165		
Large-Signal -3dB Bandwidth	BW_{LS}	V_{OUT} = 2Vp-p, R_L = 1kΩ			110		MHz
Bandwidth for 0.1dB Flatness (Note 5)	$BW_{0.1dB}$	R_L = 1kΩ		20	50		MHz
		R_L = 150Ω			40		
Slew Rate (Note 5)	SR	V_{OUT} = 2V step, R_L = 1kΩ	Rising edge	260	340		V/µs
			Falling edge	220	300		
Settling Time to 0.1%	t_S	V_{OUT} = 2V step, R_L = 1kΩ			20		ns
Rise/Fall Time	t_R and t_F	V_{OUT} = 2V step, R_L = 1kΩ			6		ns
Spurious-Free Dynamic Range	SFDR	f_C = 5MHz, V_{OUT} = 2Vp-p	R_L = 1kΩ		72		dB
			R_L = 150Ω		57		
Second Harmonic Distortion		f_C = 5MHz, V_{OUT} = 2Vp-p	R_L = 1kΩ		-80		dBc
			R_L = 150Ω		-76		
Third Harmonic Distortion		f_C = 5MHz, V_{OUT} = 2Vp-p	R_L = 1kΩ		-72		dBc
			R_L = 150Ω		-57		
Differential Phase Error	DP	NTSC	R_L = 1kΩ		0.01		degrees
			R_L = 150Ω		0.35		

AC ELECTRICAL CHARACTERISTICS—Single Supply (MAX4180/4182/4183/4186) (cont.)

(V_{CC} = +5V, V_{EE} = 0, V_{IN+} = 2.5V, $\overline{SHDN}$ ≥ 3V, A_V = +2V/V; see Table 1 for R_F and R_G values; T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Differential Gain Error	DG	NTSC	R_L = 1kΩ		0.10		%
			R_L = 150Ω		0.03		
Input Noise-Voltage Density	e_n	f = 10kHz			2		nV/√Hz
Input Noise-Current Density	i_n	f = 10kHz	IN+		4		pA/√Hz
			IN-		5		
Input Capacitance (Positive Input)	C_{IN+}				1.5		pF
Output Impedance	Z_{OUT}	f = 10kHz			4.8		Ω
Disabled Output Capacitance	$C_{OUT(OFF)}$	$\overline{SHDN}$ ≤ V_{IL}, 1.2V ≤ V_{OUT} ≤ 3.8V (Notes 2, 4)			4		pF
Turn-On Time from $\overline{SHDN}$	t_{ON}	(Note 4)			40		ns
Turn-Off Time to $\overline{SHDN}$	t_{OFF}	(Note 4)			400		ns
Power-Up Time					200		µs
Off-Isolation		$\overline{SHDN}$ ≤ 2V, R_L = 150Ω, f = 10MHz			-60		dB
Crosstalk		f = 10MHz, MAX4182/MAX4183/MAX4186			-60		dB
Gain Matching to 0.1dB		f = 10MHz, MAX4182/MAX4183/MAX4186			25		MHz

AC ELECTRICAL CHARACTERISTICS—Single Supply (MAX4181/4184/4185/4187)

(V_{CC} = +5V, V_{EE} = 0, V_{IN+} = 2.5V, $\overline{SHDN}$ ≥ 3V, A_V = +1V/V; see Table 1 for R_F values; T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Small-Signal -3dB Bandwidth (Note 5)	BW_{SS}	<0.5dB peaking	R_L = 1kΩ	175	220		MHz
			R_L = 150Ω		170		
Large-Signal -3dB Bandwidth	BW_{LS}	V_{OUT} = 2Vp-p, R_L = 1kΩ			110		MHz
Bandwidth for 0.1dB Flatness (Note 5)	$BW_{0.1dB}$	R_L = 1kΩ		16	40		MHz
		R_L = 150Ω			30		
Slew Rate (Note 5)	SR	V_{OUT} = 2V step, R_L = 1kΩ	Rising edge	210	275		V/µs
			Falling edge	170	215		
Settling Time to 0.1%	t_S	V_{OUT} = 2V step, R_L = 1kΩ			22		ns
Rise/Fall Time	t_R and t_F	V_{OUT} = 2V step, R_L = 1kΩ			7		ns
Spurious-Free Dynamic Range	SFDR	f_C = 5MHz, V_{OUT} = 2Vp-p	R_L = 1kΩ		55		dB
			R_L = 150Ω		59		
Second Harmonic Distortion		f_C = 5MHz, V_{OUT} = 2Vp-p	R_L = 1kΩ		-61		dBc
			R_L = 150Ω		-72		
Third Harmonic Distortion		f_C = 5MHz, V_{OUT} = 2Vp-p	R_L = 1kΩ		-55		dBc
			R_L = 150Ω		-59		
Differential Phase Error	DP	NTSC	R_L = 1kΩ		0.01		degrees
			R_L = 150Ω		0.35		

Single/Dual/Quad, 270MHz, 1mA, SOT23, Current-Feedback Amplifiers with Shutdown

AC ELECTRICAL CHARACTERISTICS—Single Supply (MAX4181/4184/4185/4187) (cont.)

(V_{CC} = +5V, V_{EE} = 0, V_{IN+} = 2.5V, $\overline{SHDN}$ ≥ 3V, A_V = +1V/V; see Table 1 for R_F values; T_A = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Differential Gain Error	DG	NTSC	R_L = 1kΩ		0.10		%
			R_L = 150Ω		0.03		
Input Noise-Voltage Density	e_n	f = 10kHz			2		nV/√Hz
Input Noise-Current Density	i_n	f = 10kHz	IN+		4		pA/√Hz
			IN-		5		
Input Capacitance (Positive Input)	C_{IN+}				1.5		pF
Output Impedance	Z_{OUT}	f = 10kHz			4.8		Ω
Disabled Output Capacitance	$C_{OUT(OFF)}$	$\overline{SHDN}$ ≤ V_{IL}, 1.2V ≤ V_{OUT} ≤ 3.8V (Notes 2, 4)			4		pF
Turn-On Time from $\overline{SHDN}$	t_{ON}	(Note 4)			40		ns
Turn-Off Time to $\overline{SHDN}$	t_{OFF}	(Note 4)			400		ns
Power-Up Time					200		µs
Off-Isolation		$\overline{SHDN}$ ≤ 2V, R_L = 150Ω, f = 10MHz			-54		dB
Crosstalk		f = 10MHz, MAX4184/MAX4185/MAX4187			-60		dB
Gain Matching to 0.1dB		f = 10MHz, MAX4184/MAX4185/MAX4187			25		MHz

Note 1: The MAX418_EUT is 100% production tested at T_A = +25°C. Specifications over temperature limits are guaranteed by design.

Note 2: Does not include current into the external-feedback network.

Note 3: Over operating supply-voltage range.

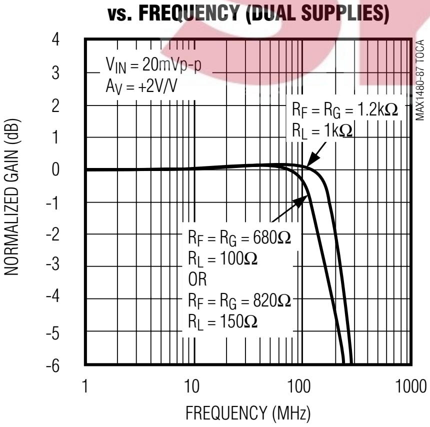
Note 4: Specification applies to MAX4180/MAX4181/MAX4183 and MAX4185.

Note 5: The AC specifications shown are not measured in a production test environment. The minimum AC specifications given are based on the combination of worst-case design simulations along with a sample characterization of units. These minimum specifications are for design guidance only and are not intended to guarantee AC performance (see *AC Testing/Performance*). For 100% testing of those parameters, contact the factory.

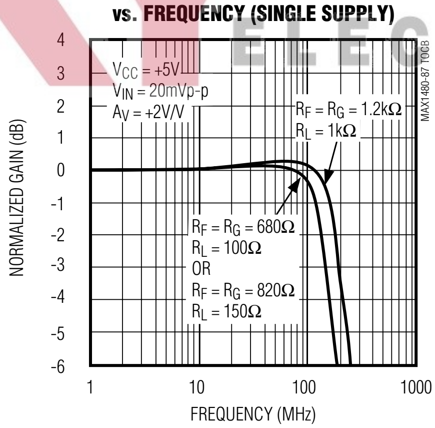
Typical Operating Characteristics

(V_{CC} = +5V, V_{EE} = -5V, T_A = +25°C, unless otherwise noted.)

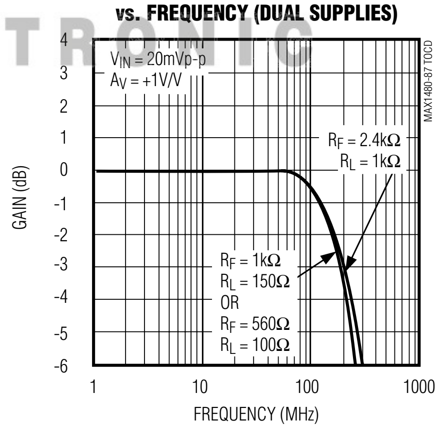
MAX4180 SMALL-SIGNAL GAIN vs. FREQUENCY (DUAL SUPPLIES)



MAX4180 SMALL-SIGNAL GAIN vs. FREQUENCY (SINGLE SUPPLY)



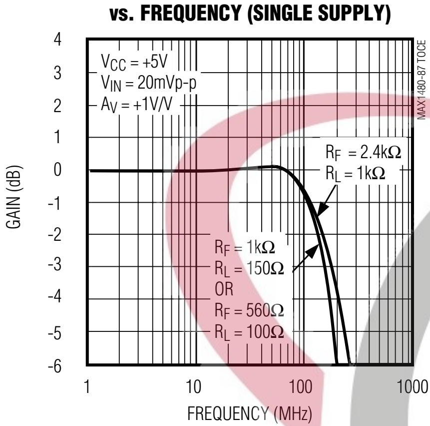
MAX4181 SMALL-SIGNAL GAIN vs. FREQUENCY (DUAL SUPPLIES)



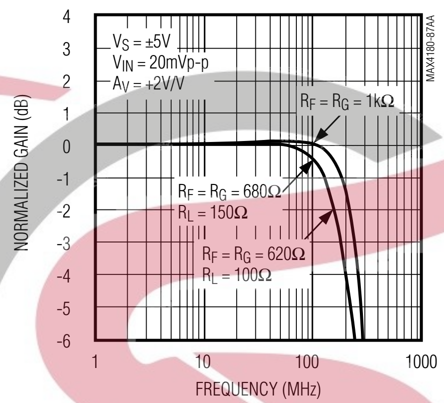
Typical Operating Characteristics (continued)

(V_{CC} = +5V, V_{EE} = -5V, T_A = +25°C, unless otherwise noted.)

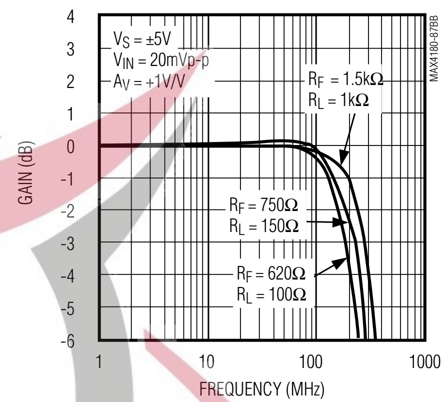
MAX4181 SMALL-SIGNAL GAIN vs. FREQUENCY (SINGLE SUPPLY)



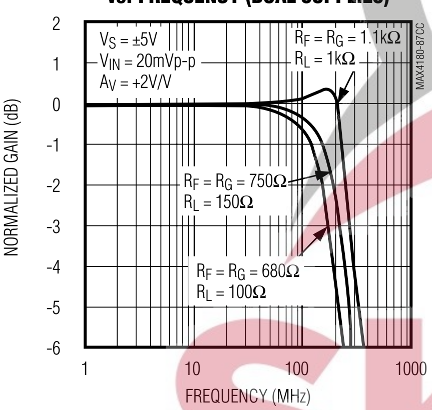
MAX4182/MAX4183 SMALL-SIGNAL GAIN vs. FREQUENCY (DUAL SUPPLIES)



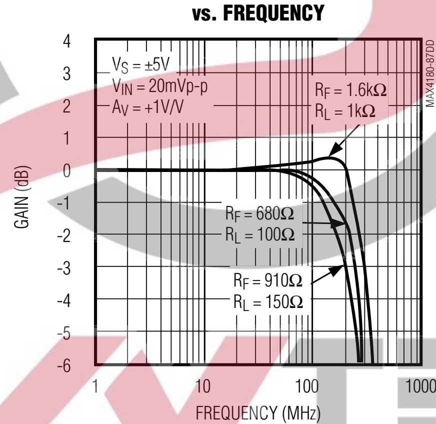
MAX4184/MAX4185 SMALL-SIGNAL GAIN vs. FREQUENCY (DUAL SUPPLIES)



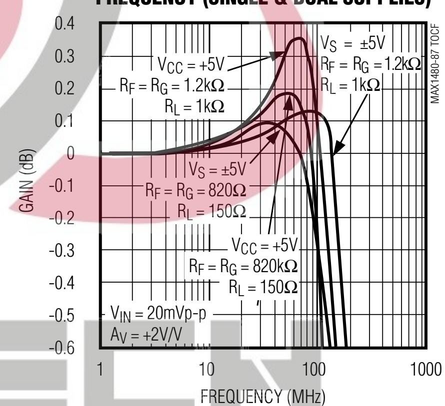
MAX4186 SMALL-SIGNAL GAIN vs. FREQUENCY (DUAL SUPPLIES)



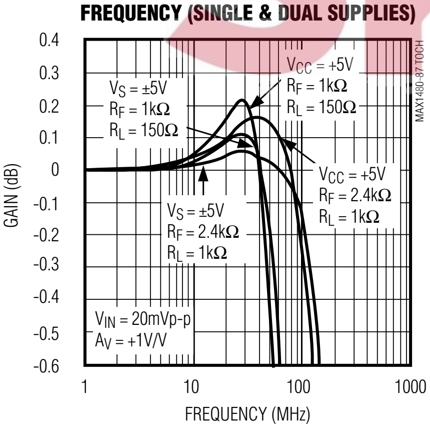
MAX4187 SMALL-SIGNAL GAIN vs. FREQUENCY



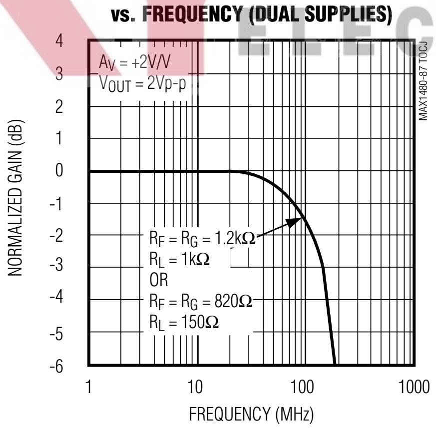
MAX4180 GAIN FLATNESS vs. FREQUENCY (SINGLE & DUAL SUPPLIES)



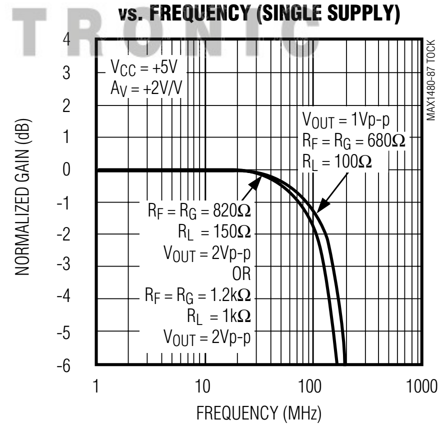
MAX4181 GAIN FLATNESS vs. FREQUENCY (SINGLE & DUAL SUPPLIES)



MAX4180 LARGE-SIGNAL GAIN vs. FREQUENCY (DUAL SUPPLIES)



MAX4180 LARGE-SIGNAL GAIN vs. FREQUENCY (SINGLE SUPPLY)

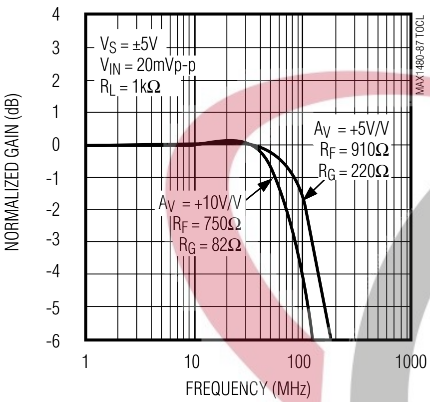


Single/Dual/Quad, 270MHz, 1mA, SOT23, Current-Feedback Amplifiers with Shutdown

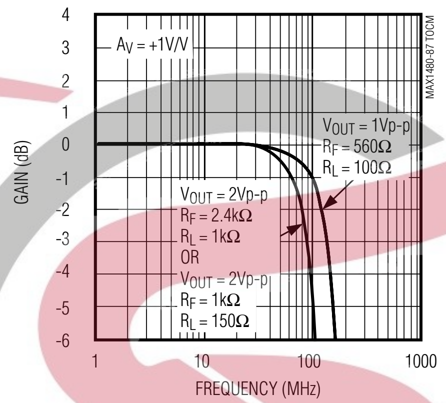
Typical Operating Characteristics (continued)

(V_{CC} = +5V, V_{EE} = -5V, T_A = +25°C, unless otherwise noted.)

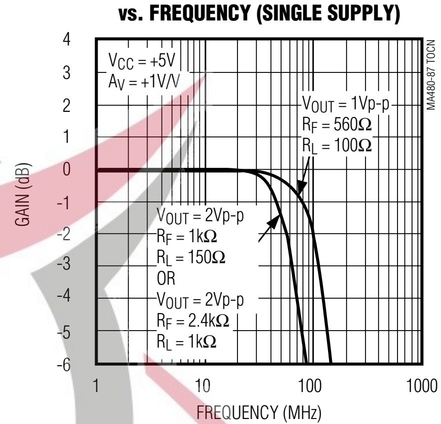
MAX4180 SMALL-SIGNAL GAIN vs. FREQUENCY



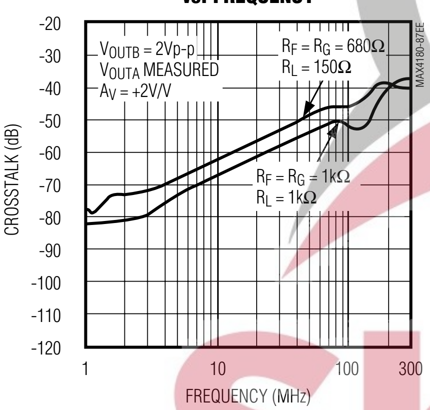
MAX4181 LARGE-SIGNAL GAIN vs. FREQUENCY (DUAL SUPPLIES)



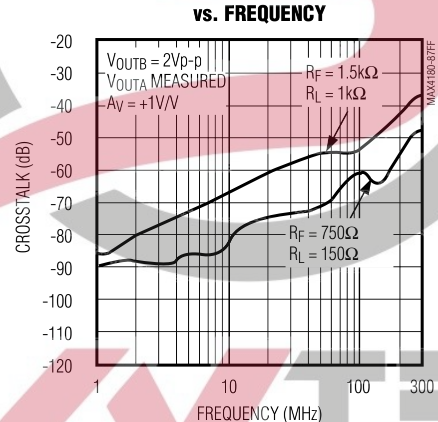
MAX4181 LARGE-SIGNAL GAIN vs. FREQUENCY (SINGLE SUPPLY)



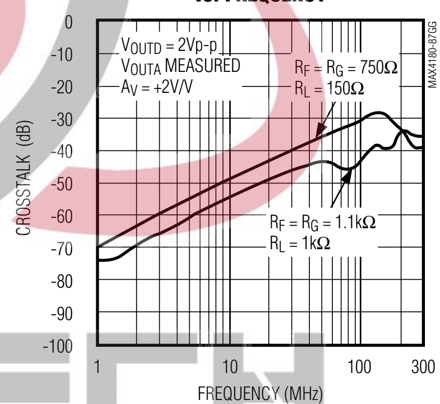
MAX4182 CROSSTALK vs. FREQUENCY



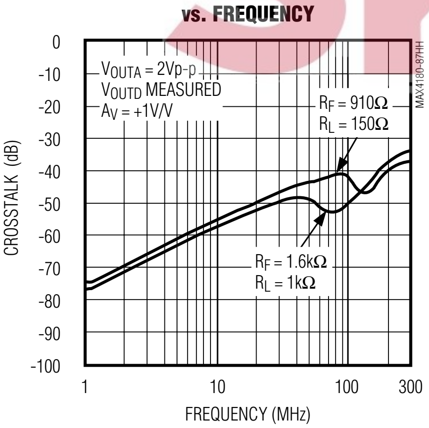
MAX4184 CROSSTALK vs. FREQUENCY



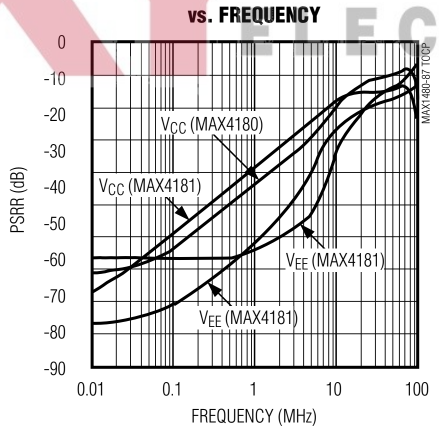
MAX4186 CROSSTALK vs. FREQUENCY



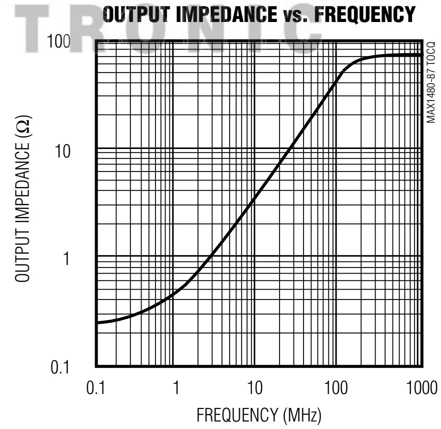
MAX4187 CROSSTALK vs. FREQUENCY



POWER-SUPPLY REJECTION RATIO vs. FREQUENCY



OUTPUT IMPEDANCE vs. FREQUENCY

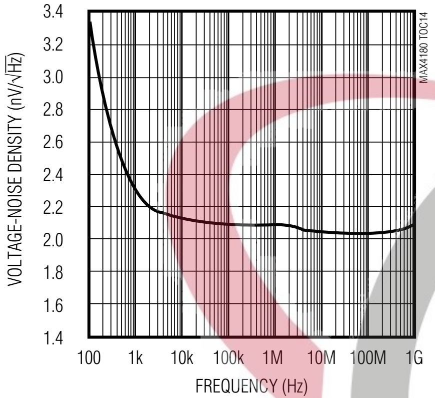


MAXIM

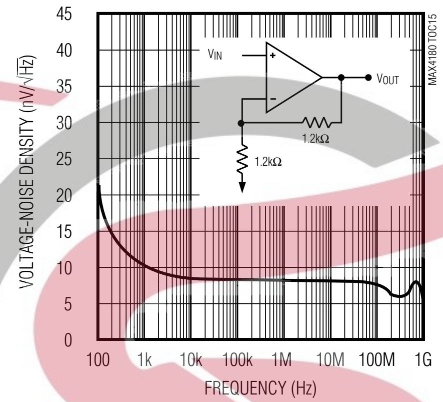
Typical Operating Characteristics (continued)

(V_{CC} = +5V, V_{EE} = -5V, T_A = +25°C, unless otherwise noted.)

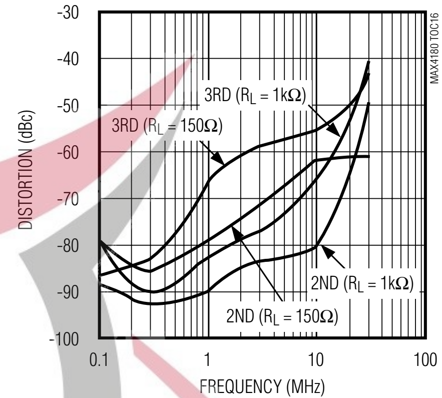
VOLTAGE-NOISE DENSITY vs. FREQUENCY (INPUT REFERRED)



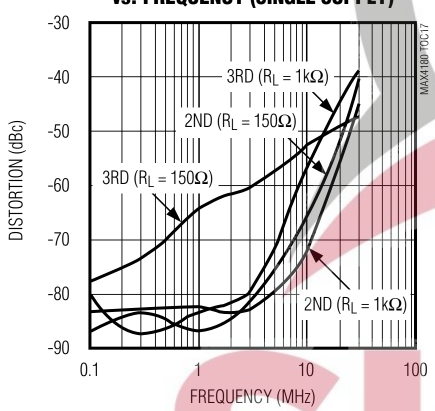
TOTAL VOLTAGE-NOISE DENSITY vs. FREQUENCY (INPUT REFERRED)



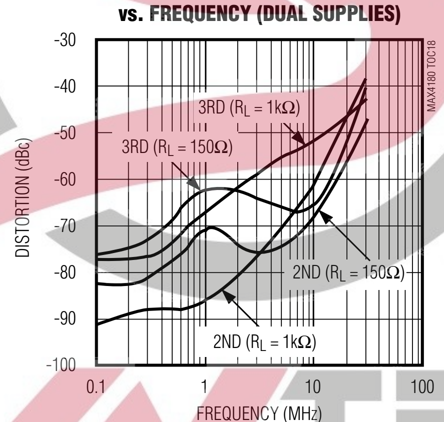
MAX4180 HARMONIC DISTORTION vs. FREQUENCY (DUAL SUPPLIES)



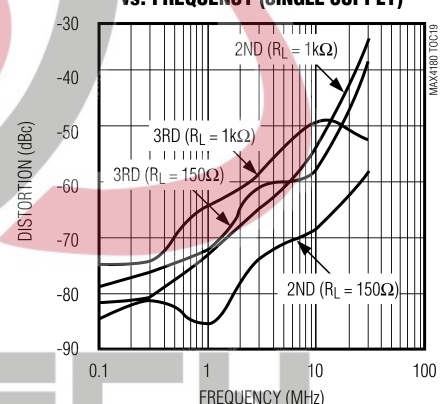
MAX4180 HARMONIC DISTORTION vs. FREQUENCY (SINGLE SUPPLY)



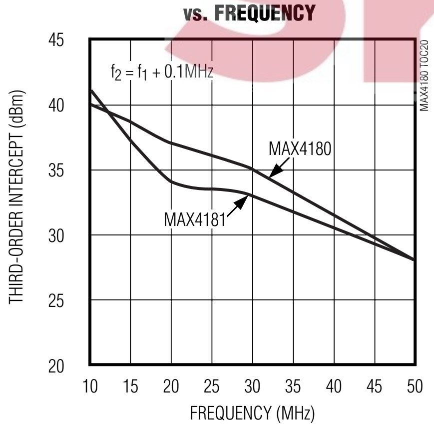
MAX4181 HARMONIC DISTORTION vs. FREQUENCY (DUAL SUPPLIES)



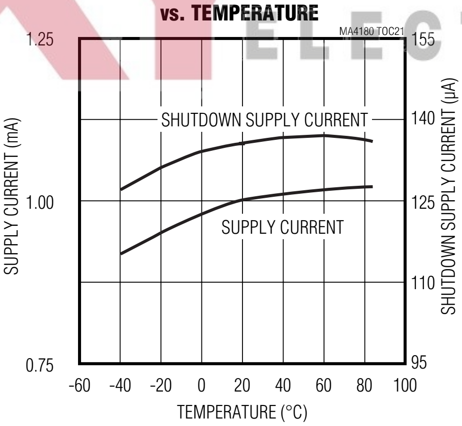
MAX4181 HARMONIC DISTORTION vs. FREQUENCY (SINGLE SUPPLY)



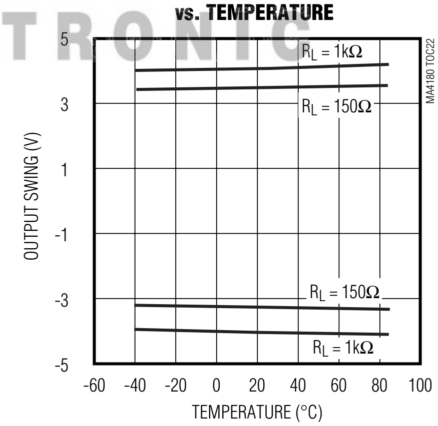
TWO-TONE THIRD-ORDER INTERCEPT vs. FREQUENCY



SUPPLY CURRENT (OPERATING & SHUTDOWN) vs. TEMPERATURE



OUTPUT VOLTAGE SWING vs. TEMPERATURE

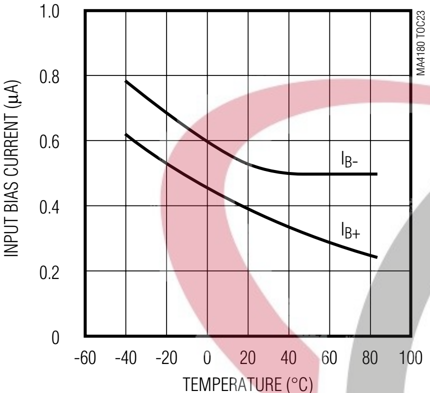


Single/Dual/Quad, 270MHz, 1mA, SOT23, Current-Feedback Amplifiers with Shutdown

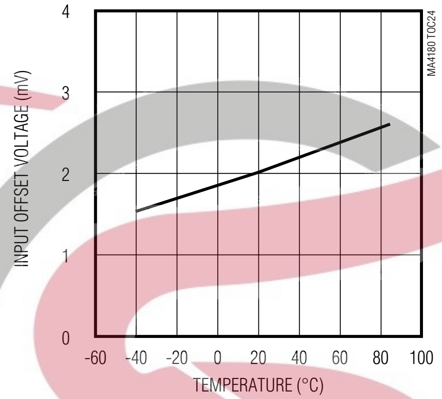
Typical Operating Characteristics (continued)

(V_{CC} = +5V, V_{EE} = -5V, T_A = +25°C, unless otherwise noted.)

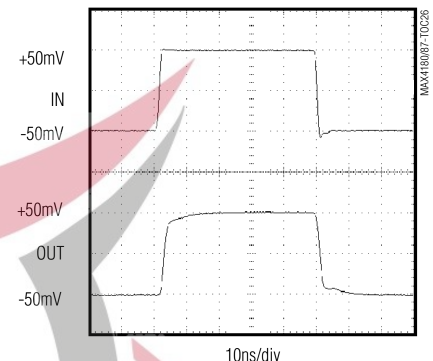
INPUT BIAS CURRENT vs. TEMPERATURE



INPUT OFFSET VOLTAGE vs. TEMPERATURE

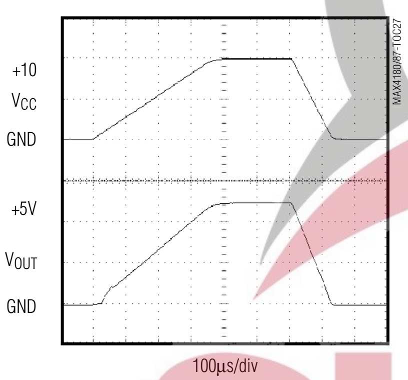


MAX4181 SMALL-SIGNAL PULSE RESPONSE



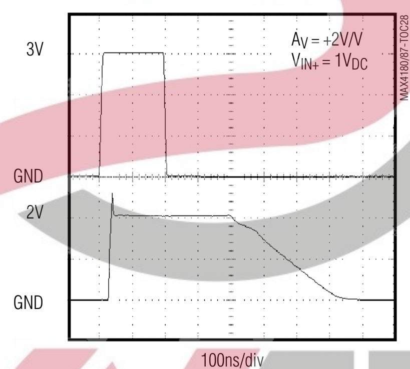
R_F = 1kΩ, R_L = 150Ω

POWER-ON TRANSIENT



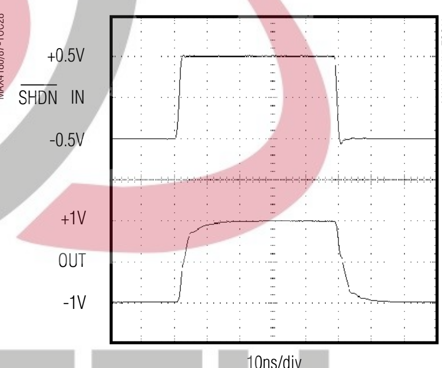
R_F = 1kΩ, V_{IN} = V_{CC}/2, R_L = ∞

SHUTDOWN RESPONSE TIME



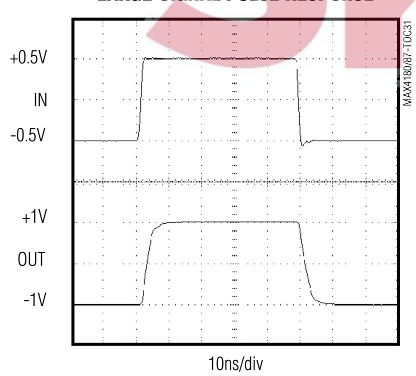
R_L = 150Ω, R_F = R_G = 820Ω

MAX4180 LARGE-SIGNAL PULSE RESPONSE



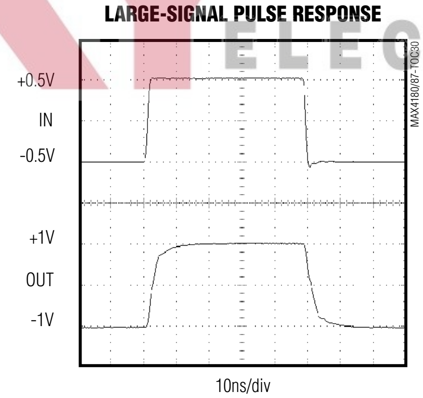
R_L = 1kΩ, R_F = R_G = 1.2kΩ

MAX4180 LARGE-SIGNAL PULSE RESPONSE



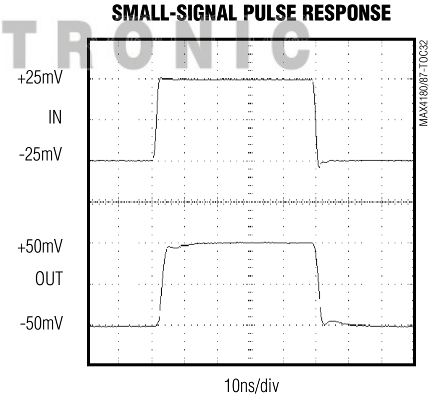
R_L = 100Ω, R_F = R_G = 680Ω

MAX4180 LARGE-SIGNAL PULSE RESPONSE



R_L = 150Ω, R_F = R_G = 820Ω

MAX4180 SMALL-SIGNAL PULSE RESPONSE

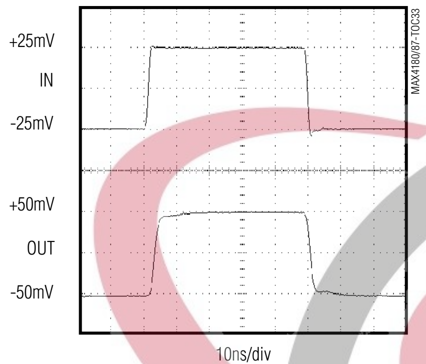


R_L = 1kΩ, R_F = R_G = 1.2kΩ

Typical Operating Characteristics (continued)

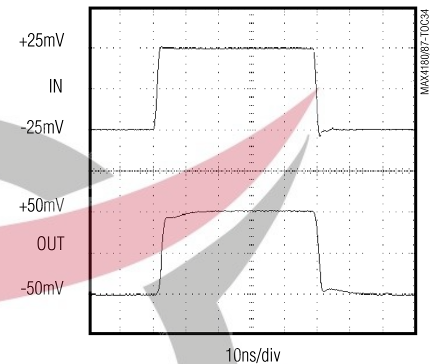
(V_{CC} = +5V, V_{EE} = -5V, T_A = +25°C, unless otherwise noted.)

MAX4180
SMALL-SIGNAL PULSE RESPONSE



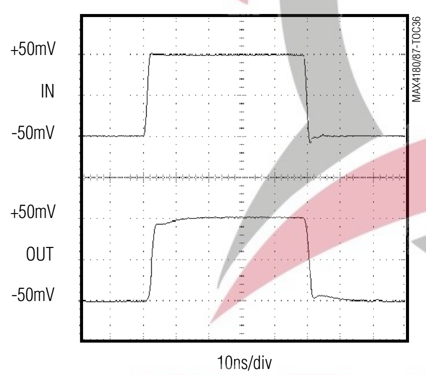
R_L = 150Ω, R_F = R_G = 820Ω

MAX4180
SMALL-SIGNAL PULSE RESPONSE



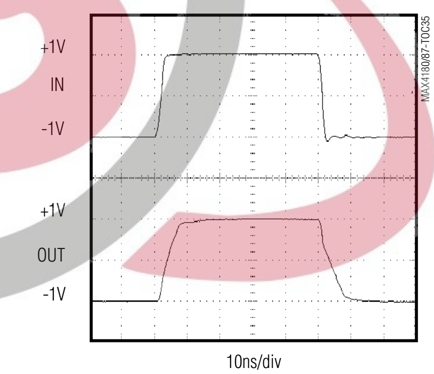
R_L = 100Ω, R_F = R_G = 680Ω

MAX4181
SMALL-SIGNAL PULSE RESPONSE



R_L = 1kΩ, R_F = 2.4kΩ

MAX4181
LARGE-SIGNAL PULSE RESPONSE



V_S = ±5V, R_L = 1kΩ, R_F = 2.4kΩ

Pin Description

MAX4180/MAX4181

PIN		NAME	FUNCTION
MAX4180/MAX4181			
SO	SOT23-6		
1, 5	—	N.C.	No Connection. Not internally connected.
2	4	IN-	Inverting Input
3	3	IN+	Noninverting Input
4	2	V_{EE}	Negative Power Supply. Connect V_{EE} to -5V or ground for single-supply operation.
6	1	OUT	Amplifier Output
7	6	V_{CC}	Positive Power Supply. Connect V_{CC} to +5V.
8	5	$\overline{SHDN}$	Shutdown Input. Device is enabled when $\overline{SHDN}$ ≥ (V_{CC} - 2V) and disabled when $\overline{SHDN}$ ≤ (V_{CC} - 3V).

Single/Dual/Quad, 270MHz, 1mA, SOT23, Current-Feedback Amplifiers with Shutdown

Pin Description (continued)

MAX4182/MAX4183/MAX4184/MAX4185

PIN			NAME	FUNCTION
MAX4182 MAX4184	MAX4183 MAX4185	MAX4183 MAX4185		
SO	SO	μMAX		
1	1	1	OUTA	Amplifier A Output
2	2	2	INA-	Amplifier A Inverting Input
3	3	3	INA+	Amplifier A Noninverting Input
4	4	4	V_{EE}	Negative Power Supply. Connect V_{EE} to -5V or ground for single-supply operation.
—	5, 7, 8, 10	—	N.C.	No Connection. Not internally connected.
—	6	5	$\overline{SHDNA}$	Shutdown Control Input for Amplifier A. Amplifier A is enabled when $\overline{SHDNA} \geq (V_{CC} - 2V)$ and disabled when $\overline{SHDNA} \leq (V_{CC} - 3V)$.
—	9	6	$\overline{SHDNB}$	Shutdown Control Input for Amplifier B. Amplifier B is enabled when $\overline{SHDNB} \geq (V_{CC} - 2V)$ and disabled when $\overline{SHDNB} \leq (V_{CC} - 3V)$.
5	11	7	INB+	Amplifier B Noninverting Input
6	12	8	INB-	Amplifier B Inverting Input
7	13	9	OUTB	Amplifier B Output
8	14	10	V_{CC}	Positive Power Supply. Connect V_{CC} to +5V.

MAX4186/MAX4187

PIN		NAME	FUNCTION
MAX4186 MAX4187	MAX4186 MAX4187		
SO	QSOP		
1	1	OUTA	Amplifier A Output
2	2	INA-	Amplifier A Inverting Input
3	3	INA+	Amplifier A Noninverting Input
4	4	V_{CC}	Positive Power Supply. Connect V_{CC} to +5V.
5	5	INB+	Amplifier B Noninverting Input
6	6	INB-	Amplifier B Inverting Input
7	7	OUTB	Amplifier B Output
—	8, 9	N.C.	No Connection. Not internally connected.
8	10	OUTC	Amplifier C Output
9	11	INC-	Amplifier C Inverting Input
10	12	INC+	Amplifier C Noninverting Input
11	13	V_{EE}	Negative Power Supply. Connect V_{EE} to -5V or ground for single-supply operation.
12	14	IND+	Amplifier D Noninverting Input
13	15	IND-	Amplifier D Inverting Input
14	16	OUTD	Amplifier D Output

Detailed Description

The MAX4180–MAX4187 are ultra-low-power current-feedback amplifiers featuring bandwidths up to 270MHz, 0.1dB gain flatness to 90MHz, and low differential gain (0.08%) and phase (0.03°) errors. These amplifiers achieve ultra-high bandwidth-to-power ratios with low distortion, wide signal swing, and excellent load-driving capabilities. They are optimized for ±5V supplies but also operate from a single +5V supply while consuming only 1mA per amplifier. With ±60mA output current drive capability, the devices achieve low distortion even while driving 150Ω loads.

Wide bandwidth, low power, low differential phase and gain error, and excellent gain flatness make the MAX4180–MAX4187 ideal for use in portable video equipment such as cameras, video switchers, and other battery-powered applications. Their two-stage design provides higher gain and lower distortion than conventional single-stage, current-feedback topologies. This feature, combined with fast settling time, makes these devices suitable for buffering high-speed analog-to-digital converters (ADCs).

The MAX4180/MAX4181/MAX4183/MAX4185 have a low-power shutdown mode that is activated by driving the amplifiers' $\overline{SHDN}$ input low. Placing them in shutdown reduces quiescent supply current to 135µA (typ) and places amplifier outputs in a high-impedance state. These amplifiers can be used to implement a high-speed multiplexer by connecting together the outputs of multiple amplifiers and controlling the $\overline{SHDN}$ inputs to enable one amplifier and disable all the others. The disabled amplifiers present very little load (0.1µA leakage current and 4pF capacitance) to the active amplifiers' output. Note that the feedback network impedance of all the disabled amplifiers must be considered when calculating the total load on the active amplifier output.

Application Information

Theory of Operation

The MAX4180–MAX4187 are current-feedback amplifiers, and their open-loop transfer function is expressed as a transimpedance, $\Delta V_{OUT}/\Delta I_{IN}$, or T_Z. The frequency behavior of the open-loop transimpedance is similar to the open-loop gain of a voltage-mode feedback amplifier. That is, it has a large DC value and decreases at approximately 6dB per octave.

Analyzing the follower with gain, as shown in Figure 1, yields the following transfer function:

$$V_{OUT} / V_{IN} = G \times [(T_Z (S) / T_Z(s) + G \times (R_{IN} + R_F)]$$

where $G = A_{VCL} = 1 + (R_F / R_G)$, and $R_{IN} = 1 / g_M \cong 160\Omega$.

At low gains, $G \times R_{IN} < R_F$. Therefore, the closed-loop bandwidth is essentially independent of closed-loop gain. Similarly, $T_Z > R_F$ at low frequencies, so that:

$$\frac{V_{OUT}}{V_{IN}} = G = 1 + (R_F / R_G)$$

Layout and Power-Supply Bypassing

The MAX4180–MAX4187 have an RF bandwidth and, consequently, require careful board layout, including the possible use of constant-impedance microstrip or stripline techniques.

To realize the full AC performance of these high-speed amplifiers, pay careful attention to power-supply bypassing and board layout. The PC board should have at least two layers: a signal and power layer on one side, and a large, low-impedance ground plane on the other side. The ground plane should be as free of voids as possible. With multilayer boards, locate the ground plane on a layer that incorporates no signal or power traces.

Regardless of whether a constant-impedance board is used, observe the following guidelines when designing the board:

- Do not use wire-wrap boards. They are too inductive.
- Do not use breadboards. They are too capacitive.
- Do not use IC sockets. They increase parasitic capacitance and inductance.
- Use surface-mount components rather than through-hole components. They give better high-frequency performance, have shorter leads, and have lower parasitic reactances.

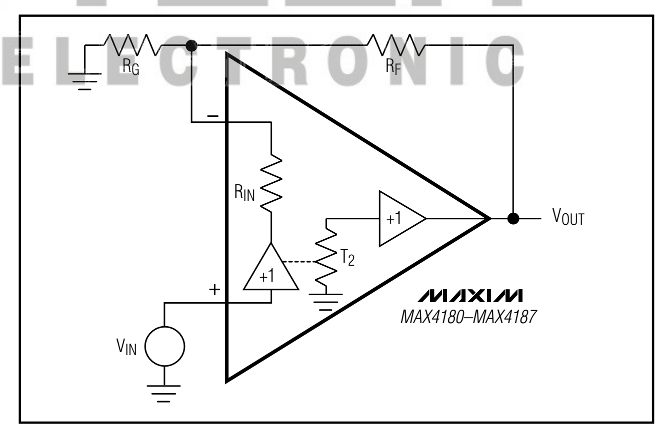


Figure 1. Current-Feedback Amplifier

Single/Dual/Quad, 270MHz, 1mA, SOT23, Current-Feedback Amplifiers with Shutdown

- Keep lines as short and as straight as possible.
- Do not make 90° turns; round all corners.
- Observe high-frequency bypassing techniques to maintain the amplifiers' accuracy. The bypass capacitors should include a 0.01µF to 0.1µF ceramic capacitor between each supply pin and the ground plane, located as close to the package as possible.
- Place a 1µF ceramic capacitor in parallel with each 0.01µF to 0.1µF capacitor as close to them as possible.
- Place a 10µF to 15µF low-ESR tantalum at the point of entry to the power-supply pins' PC board. The power-supply trace should lead directly from the tantalum capacitor to the V_{CC} and V_{EE} pins.
- Keep PC traces short and use surface-mount components to minimize parasitic inductance.

Maxim's High-Speed Evaluation Board

Figures 2 and 3 show layouts of Maxim's high-speed single SOT23 and SO evaluation boards. These boards were developed using the techniques described above. The smallest available surface-mount resistors were used for feedback and back-termination to minimize their distance from the part, reducing the capacitance associated with longer lead lengths.

SMA connectors were used for best high-frequency performance. Because distances are extremely short, performance is unaffected by the fact that inputs and outputs do not match a 50Ω line. However, in applications that require lead lengths greater than one-quarter of the wavelength of the highest frequency of interest, use constant-impedance traces.

Fully assembled evaluation boards are available for the MAX4180ESA.

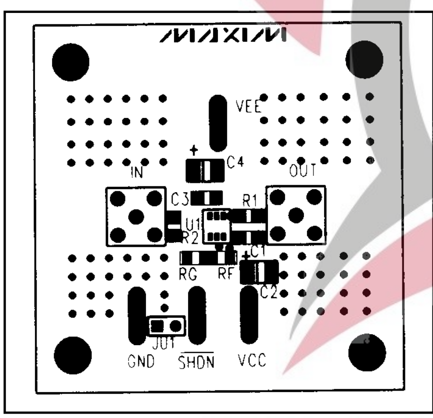


Figure 2a. SOT23 High-Speed EV Board Component Placement Guide—Component Side

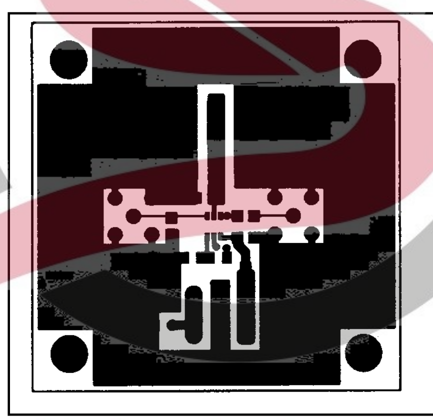


Figure 2b. SOT23 High-Speed EV Board Layout—Component Side

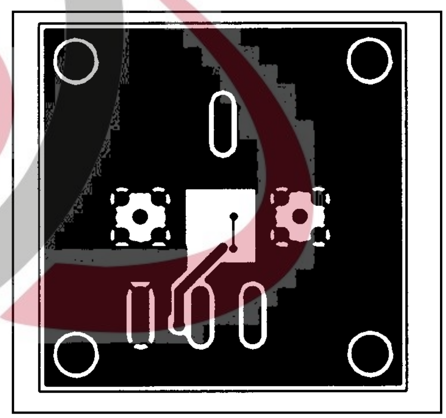


Figure 2c. High-Speed EV Board Layout—Solder Side

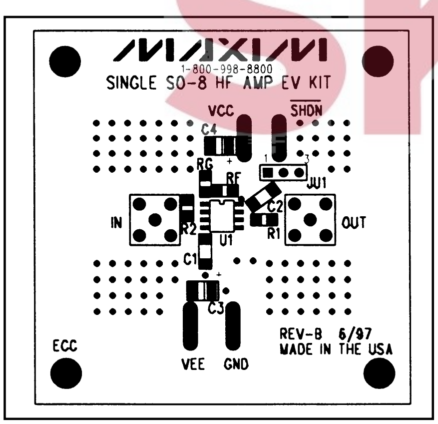


Figure 3a. SO-8 High-Speed EV Board Component Placement Guide—Component Side

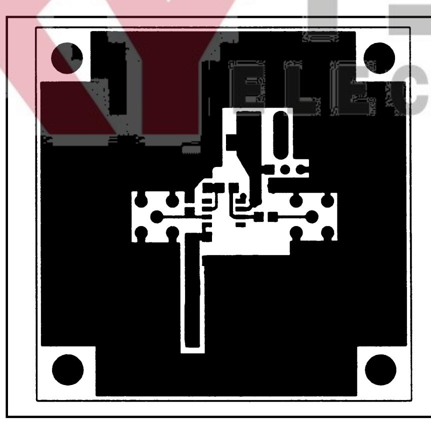


Figure 3b. SO-8 High-Speed EV Board Layout—Component Side

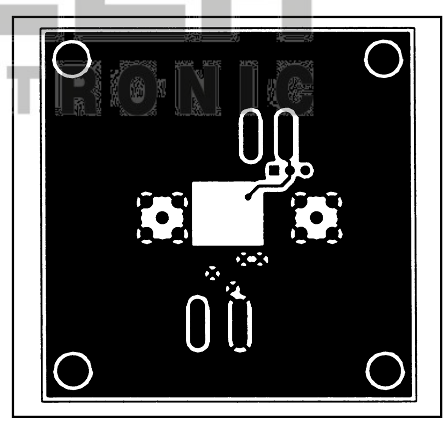


Figure 3c. SO-8 High-Speed EV Board Layout—Solder Side

Table 1. Recommended Component Values

COMPONENT/BW	MAX4180					MAX4181		
	A_V = +2V/V			A_V = +5V/V	A_V = +10V/V	A_V = +1V/V		
	R_L = 1kΩ	R_L = 150Ω	R_L = 100Ω	R_L = 1kΩ/150Ω	R_L = 1kΩ/150Ω	R_L = 1kΩ	R_L = 150Ω	R_L = 100Ω
R_F (Ω)	1.2k	820	680	520	560	2.4k	1k	560
R_G (Ω)	1.2k	820	680	130	56	—	—	—
-3dB BW (MHz)	245	190	190	120	76	270	205	200

COMPONENT/ BW	MAX4182/MAX4183			MAX4184/MAX4185			MAX4186			MAX4187		
	A_V = +2V/V			A_V = +1V/V			A_V = +2V/V			A_V = +1V/V		
	R_L = 1kΩ	R_L = 150Ω	R_L = 100Ω	R_L = 1kΩ	R_L = 150Ω	R_L = 100Ω	R_L = 1kΩ	R_L = 150Ω	R_L = 100Ω	R_L = 1kΩ	R_L = 150Ω	R_L = 100Ω
R_F (Ω)	1k	680	620	1.5k	750	620	1.1k	750	680	1.6k	910	680
R_G (Ω)	1k	680	620	—	—	—	1.1k	750	680	—	—	—
-3dB BW (MHz)	245	190	160	270	205	180	245	190	175	270	205	200

Choosing Feedback and Gain Resistors

The optimum value of the external-feedback (R_F) and gain-setting (R_G) resistors used with the MAX4180–MAX4187 depends on the closed-loop gain and the application circuit's load. Table 1 lists the optimum resistor values for some specific gain configurations. One-percent resistor values are preferred to maintain consistency over a wide range of production lots. Figures 4a and 4b show the standard inverting and noninverting configurations. **Note:** The noninverting circuit gain (Figure 4) is 1 plus the magnitude of the inverting closed-loop gain. Otherwise, the two circuits are identical.

DC and Noise Errors

Several major error sources must be considered in any op amp. These apply equally to the MAX4180–MAX4187. Offset-error terms are given by the equation below. Voltage and current-noise errors are root-square summed and are therefore computed separately. In Figure 5, the total output offset voltage is determined by the following factors:

- The input offset voltage (V_{OS}) times the closed-loop gain ($1 = R_F / R_G$).
- The positive input bias current (I_{B+}) times the source resistor (R_S) (usually 50Ω or 75Ω), plus the negative input bias current (I_{B-}) times the parallel combination of R_G and R_F. In current-feedback amplifiers, the input bias currents at the IN+ and IN- terminals do not track each other and may have opposite polarity, so there is no benefit to matching the resistance at both inputs.

The equation for the total DC error at the output is:

$$V_{OUT} = \left[(I_{B+})R_S + (I_{B-})(R_F \,||\, R_G) + V_{OS}\right]\left(1 + \frac{R_F}{R_G}\right)$$

The total output-referred noise voltage is:

$$e_{n(OUT)} = \left(1 + \frac{R_F}{R_G}\right)\sqrt{\left[(i_{n+})R_S\right]^2 + \left[(i_{n-})R_F \,||\, R_G\right]^2 + (e_n)^2}$$

The MAX4180–MAX4187 have a very low, $2nV/\sqrt{Hz}$ noise voltage. The current noise at the positive input (i_{n+}) is $4pA/\sqrt{Hz}$, and the current noise at the inverting input is $5pA/\sqrt{Hz}$.

An example of the DC error calculations, using the MAX4180 typical data and typical operating circuit where $R_F = R_G = 1.2k\Omega$ ($R_F \parallel R_G = 600\Omega$) and $R_S = 37.5\Omega$, gives the following:

$$V_{OUT} = \left[\left(1x10^{-6}\right)x37.5 + \left(2x10^{-6}\right)x\left(600\right) + 1.5x10^{-3}\right]x\left(1+1\right)$$

$$V_{OUT} = 4.1mV$$

Calculating the total output noise in a similar manner yields:

$$e_{n(OUT)} = \left(1+1\right)\sqrt{\left(4 \times 10^{-12} \times 37.5\right)^2 + \left(5 \times 10^{-12} \times 255\right)^2 + \left(2 \times 10^{-9}\right)^2}$$

$$e_{n(OUT)} = 4.8nV/\sqrt{Hz}$$

With a 200MHz system bandwidth, this calculates to $102\mu V_{RMS}$ (approximately $612\mu Vp-p$, choosing the six-sigma value).

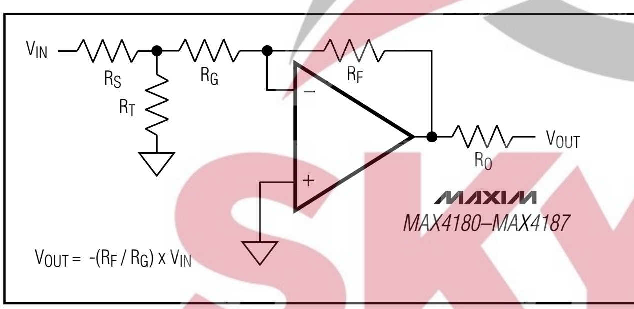


Figure 4a. Inverting Gain Configuration

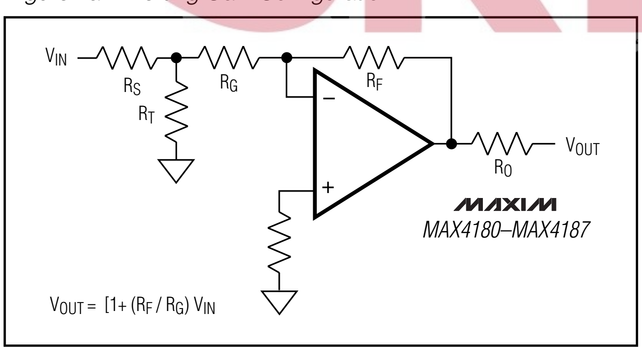


Figure 4b. Noninverting Gain Configuration

Video Line Driver

The MAX4180–MAX4187 are well suited to drive coaxial transmission lines when the cable is terminated at both ends, as shown in Figure 6. Cable-frequency response can cause variations in the signal's flatness. See Table 1 for optimum R_F and R_G values.

Driving Capacitive Loads

The MAX4180–MAX4187 are optimized for AC performance. They are not designed to drive highly capacitive loads. Reactive loads decrease phase margin and may produce excessive ringing and oscillation. Figure 7a shows a circuit that eliminates this problem. Placing the small (usually 5Ω to 22Ω) isolation resistor, R_S, before the reactive load prevents ringing and oscillation. At higher capacitive loads, the interaction of the load capacitance and isolation resistor controls AC performance. Figures 7b and 7c show the MAX4180 and MAX4181 frequency response with a 47pF capaci-

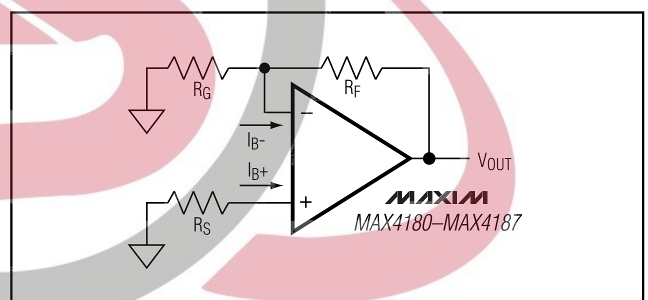


Figure 5. Output Offset Voltage

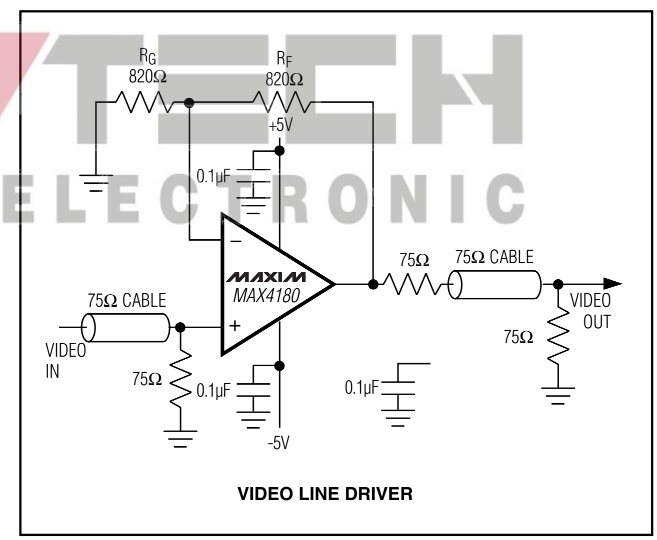


Figure 6. Video Line Driver

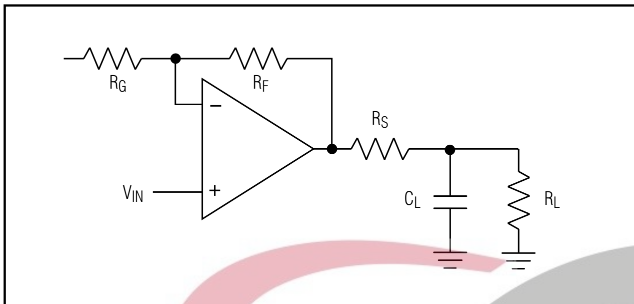


Figure 7a. Using an Isolation Resistor (R_S) for High-Capacitive Loads

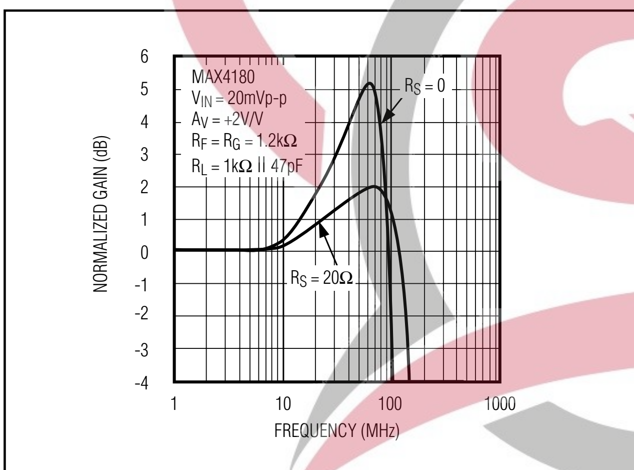


Figure 7b. Frequency Response with Capacitive Load (With and Without Isolation Resistor)

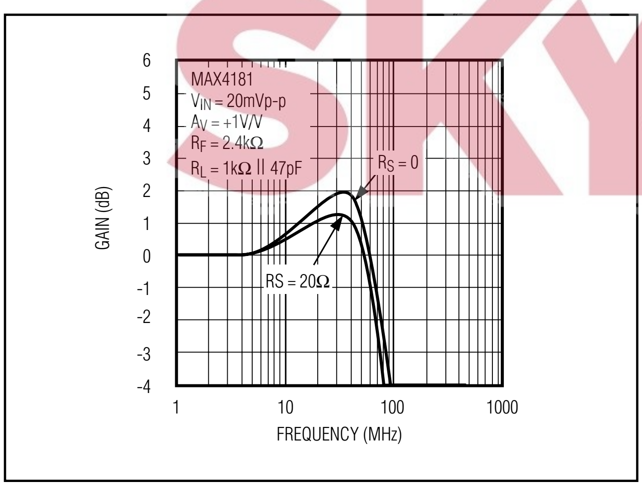


Figure 7c. Frequency Response with Capacitive Load (With and Without Isolation Resistor)

tive load. Note that in each case, gain peaking is substantially reduced when the 20Ω resistor is used to isolate the capacitive load from the amplifier output.

AC Testing/Performance

AC specifications on high-speed amplifiers are usually guaranteed without 100% production testing. Since these high-speed devices are sensitive to external parasitics introduced when automatic handling equipment is used, it is impractical to guarantee AC parameters through volume production testing. These parasitics are greatly reduced when using the recommended PC board layout (like the Maxim EV kit). Characterizing the part in this way more accurately represents the amplifier's true AC performance. Some manufacturers guarantee AC specifications without clearly stating how this guarantee is made. The AC specifications of the MAX4180–MAX4187 are derived through worst-case design simulations combined with a sample characterization of 100 units. The AC performance distributions along with the worst-case simulation results for MAX4180 and MAX4181 are shown in Figures 8–11. These distributions are repeatable provided that the proper board layout and power-supply bypassing are used (see *Layout and Power-Supply Bypassing* section).

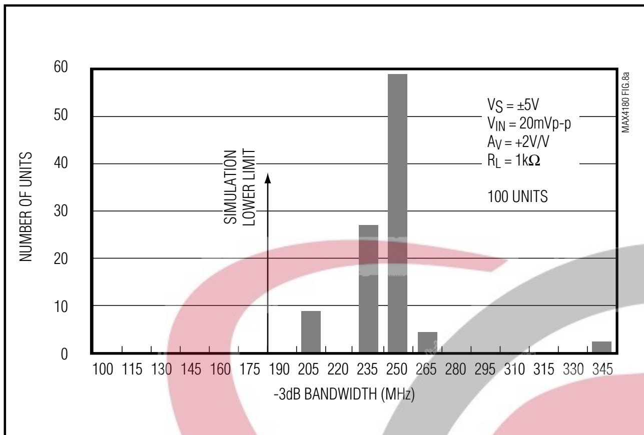


Figure 8a. MAX4180 -3dB Bandwidth Distribution (Dual Supplies)

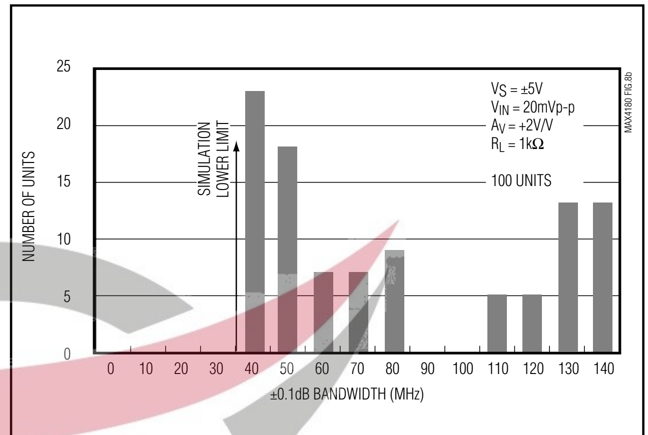


Figure 8b. MAX4180 ±0.1dB Bandwidth Distribution (Dual Supplies)

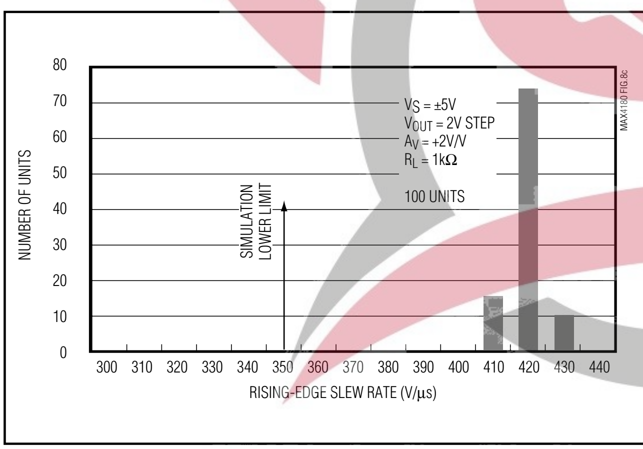


Figure 8c. MAX4180 Rising-Edge Slew-Rate Distribution (Dual Supplies)

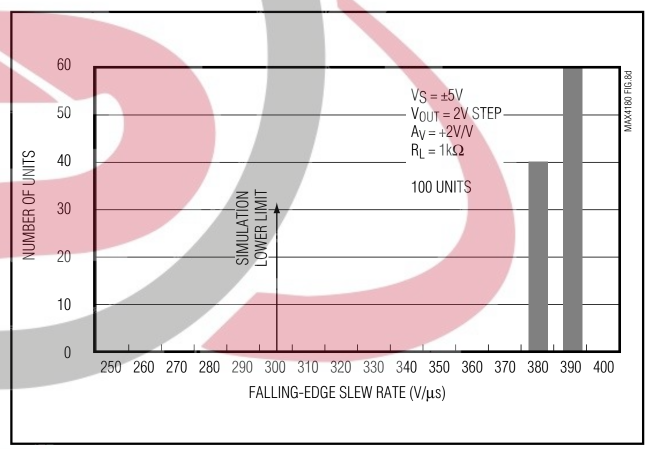


Figure 8d. MAX4180 Falling-Edge Slew-Rate Distribution (Dual Supplies)

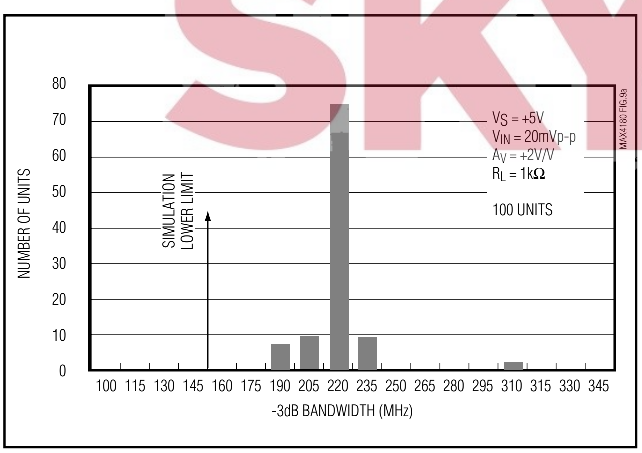


Figure 9a. MAX4180 -3dB Bandwidth Distribution (Single Supply)

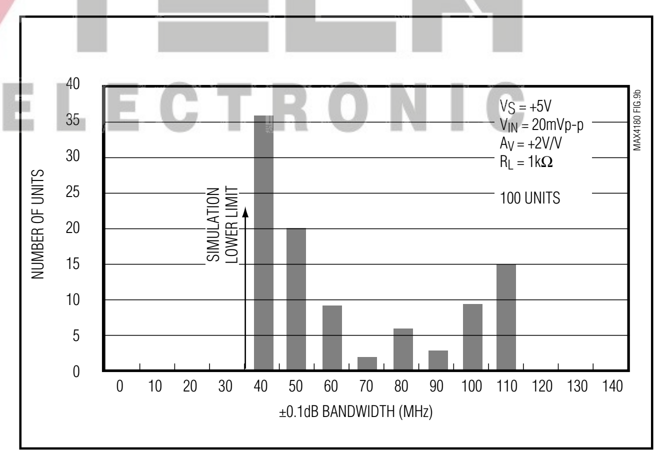


Figure 9b. MAX4180 ±0.1dB Bandwidth Distribution (Single Supply)

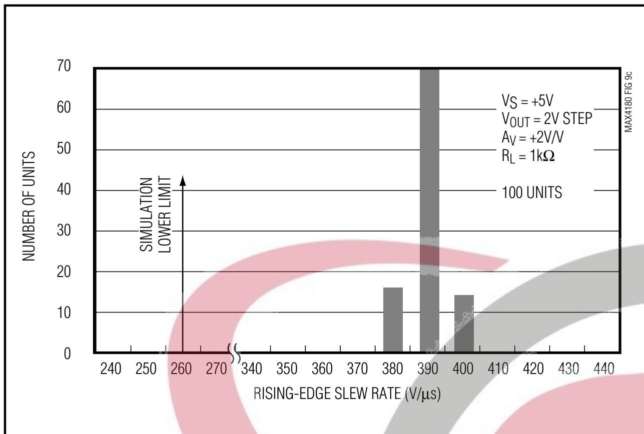


Figure 9c. MAX4180 Rising-Edge Slew-Rate Distribution (Single Supply)

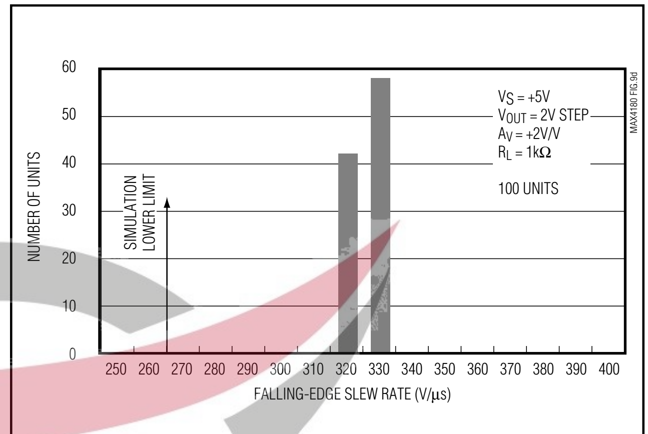


Figure 9d. MAX4180 Falling-Edge Slew-Rate Distribution (Single Supply)

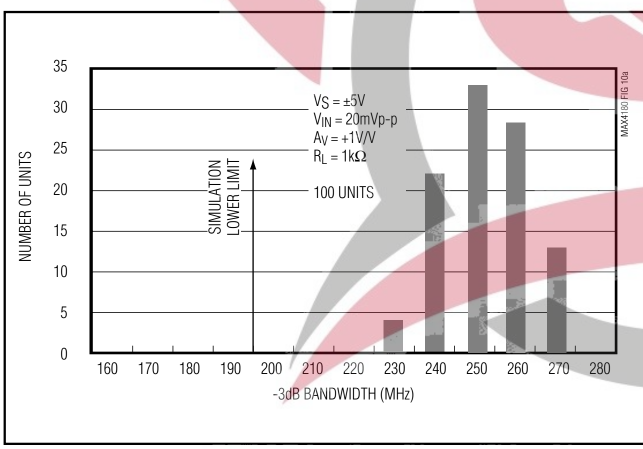


Figure 10a. MAX4181 -3dB Bandwidth Distribution (Dual Supplies)

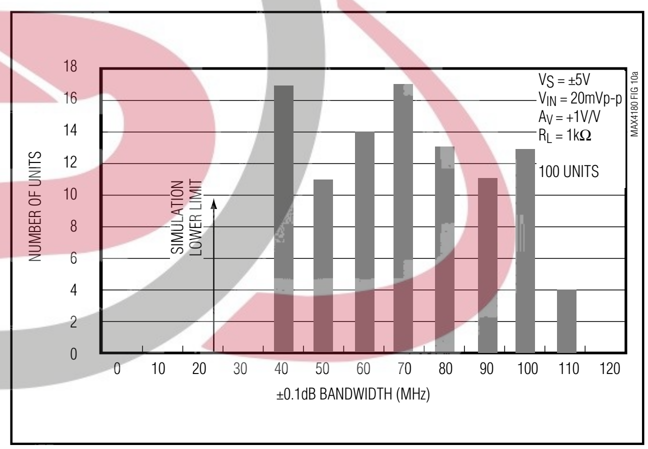


Figure 10b. MAX4181 ±0.1dB Bandwidth Distribution (Dual Supplies)

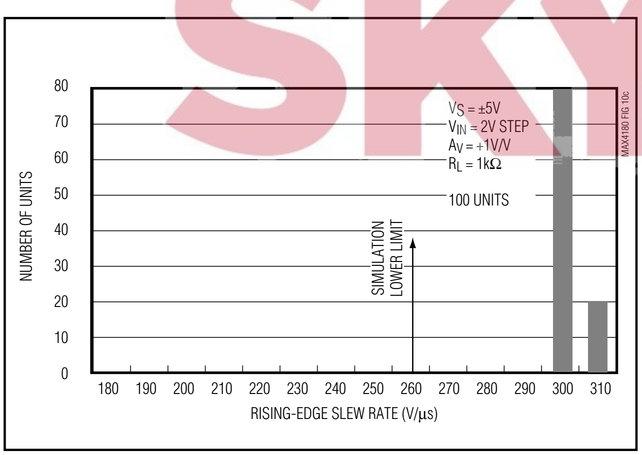


Figure 10c. MAX4181 Rising-Edge Slew-Rate Distribution (Dual Supplies)

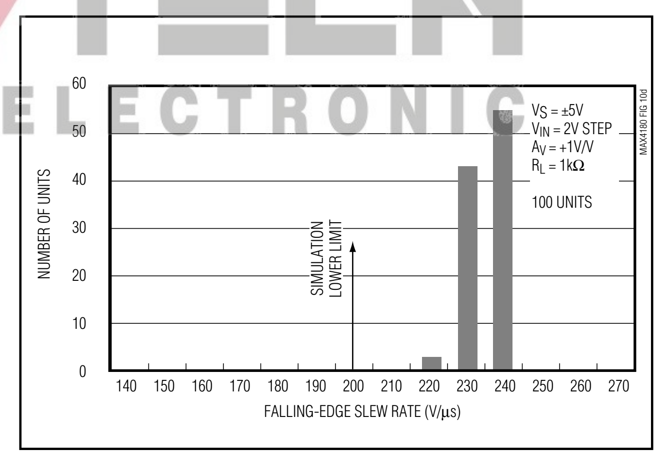


Figure 10d. MAX4181 Falling-Edge Slew-Rate Distribution (Dual Supplies)

Single/Dual/Quad, 270MHz, 1mA, SOT23, Current-Feedback Amplifiers with Shutdown

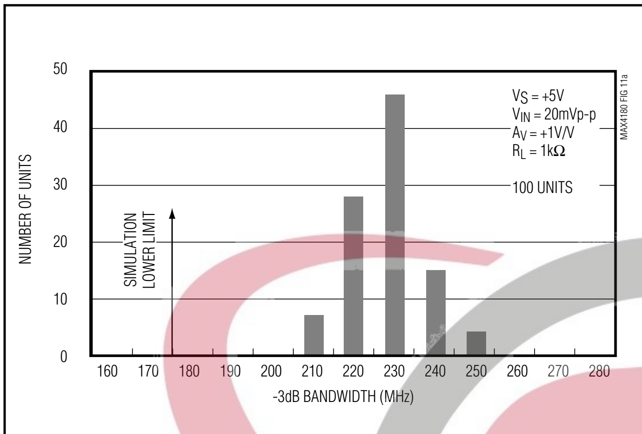


Figure 11a. MAX4181 -3dB Bandwidth Distribution (Single Supply)

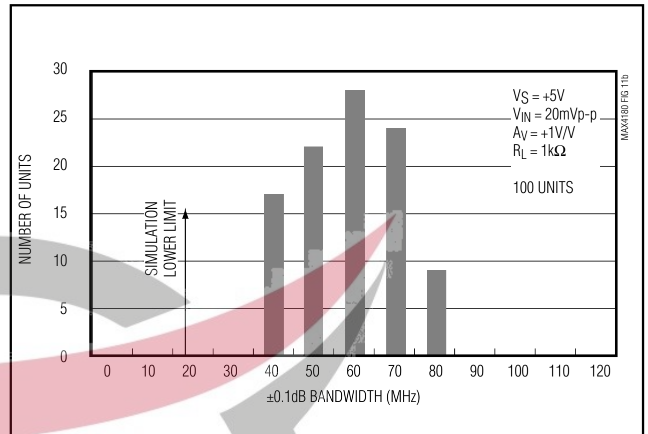


Figure 11b. MAX4181 ±0.1dB Bandwidth Distribution (Single Supply)

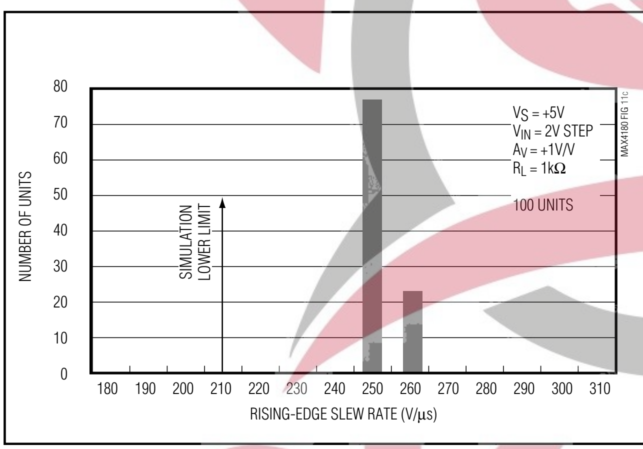


Figure 11c. MAX4181 Rising-Edge Slew-Rate Distribution (Single Supply)

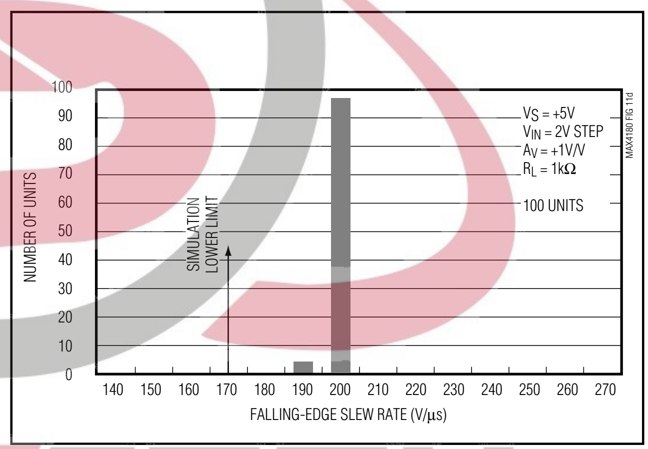
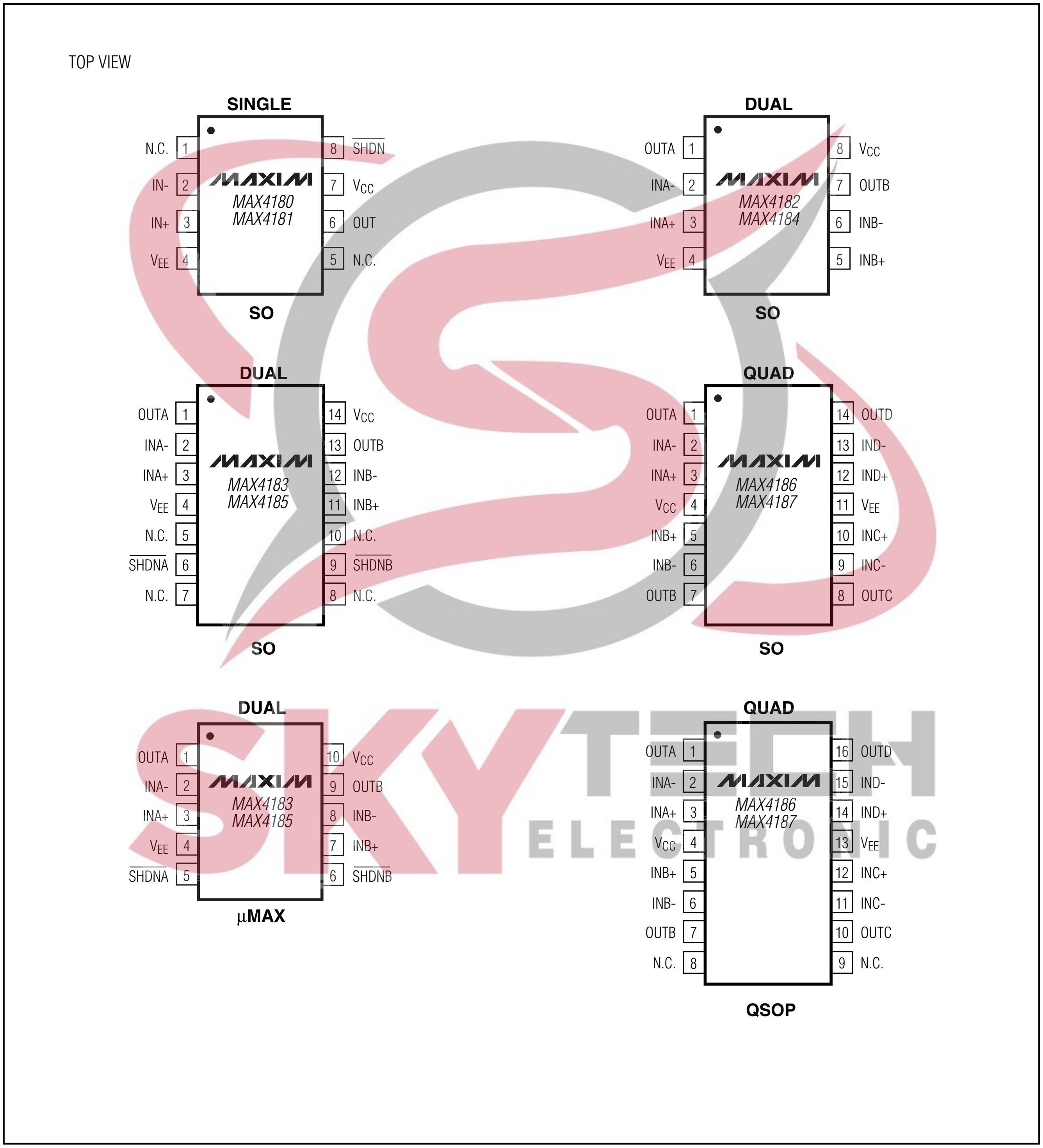


Figure 11d. MAX4181 Falling-Edge Slew-Rate Distribution (Single Supply)

Pin Configurations (continued)



Single/Dual/Quad, 270MHz, 1mA, SOT23, Current-Feedback Amplifiers with Shutdown

Ordering Information (continued)

PART	TEMP RANGE	PIN-PACKAGE	TOP MARK
MAX4181EUT-T	-40°C to +85°C	6 SOT23-6	AAAC
MAX4181ESA	-40°C to +85°C	8 SO	—
MAX4182ESA	-40°C to +85°C	8 SO	—
MAX4183EUB	-40°C to +85°C	10 µMAX*	—
MAX4183ESD	-40°C to +85°C	14 SO	—
MAX4184ESA	-40°C to +85°C	8 SO	—
MAX4185EUB	-40°C to +85°C	10 µMAX*	—
MAX4185ESD	-40°C to +85°C	14 SO	—
MAX4186ESD	-40°C to +85°C	14 SO	—
MAX4186EEE	-40°C to +85°C	16 QSOP	—
MAX4187ESD	-40°C to +85°C	14 SO	—
MAX4187EEE	-40°C to +85°C	16 QSOP	—

**Contact factory for availability.*

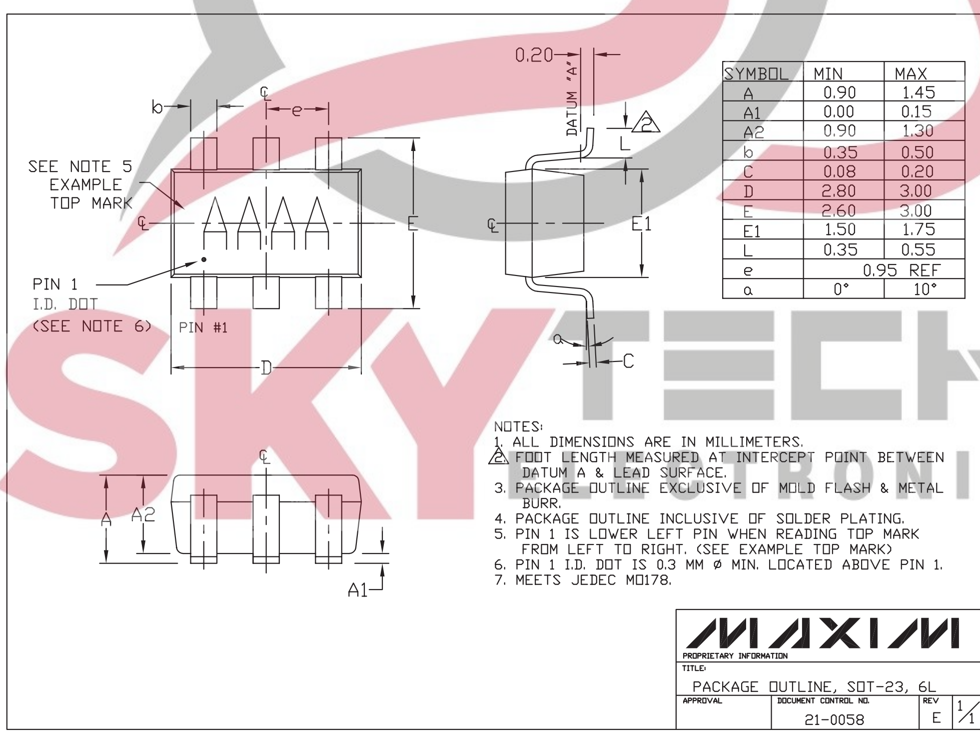
Chip Information

MAX4180/MAX4181 TRANSISTOR COUNT: 83
SUBSTRATE CONNECTED TO V_{EE}

MAX4182–MAX4185 TRANSISTOR COUNT: 166
SUBSTRATE CONNECTED TO V_{EE}

MAX4186/MAX4187 TRANSISTOR COUNT: 235
SUBSTRATE CONNECTED TO V_{EE}

Package Information



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